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Electronic Version v1.1

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Title of Invention	ISOLATION STRUCTURES IN SEMICONDUCTOR INTEGRATED CIRCUITS (IC)	
Application Number :		
Date :		
First Named Applicant:	Brent A. Anderson	
Confirmation Number:		
Attorney Docket Number:	BUR920040170US1	
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		Sign. Capacity Attorney

Documents being submitted:	Files
us-fee-sheet	BUR920040170US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-ids	BUR920040170US1-usidst.xml us-ids.dtd us-ids.xsl
us-assignment	BUR920040170US1-usassn.xml us-assignment.xsl us-assignment.dtd BUR920040170Assign1.tif BUR920040170Assign2.tif
us-request	BUR920040170US1-usrequ.xml us-request.dtd us-request.xsl
application-body	BUR920040170ABX.xml application-body.dtd Image1.tif Image2.tif isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isogr1.ent isogr2.ent isogr3.ent isogr4.ent isolat1.ent isolat2.ent isomfrk.ent isomopf.ent isomscr.ent isonum.ent isopub.ent isotech.ent mathml2.dtd mathml2-qname-1.mod mmlalias.ent mmlextra.ent soextblx.dtd us-application-body.xsl wipo.ent
application-body-pdf-wrap	BUR920040170ABX-pdf-wrap.xml
abstract-pdf	BUR920040170ABX-abst.pdf

claims-pdf	BUR920040170ABX-clms.pdf
description-pdf	BUR920040170ABX-desc.pdf
drawings-pdf	BUR920040170ABX-draw.pdf
us-declaration	BUR920040170dec1.tif
us-declaration	BUR920040170dec2.tif
Comments	
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